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2 Feb
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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

#5

In re application : John V. Reynders, et al.
Application No. : 10/027,886
Filed : October 19, 2001
For : EFFICIENT SYSTEM AND METHOD OF NODE AND
LINK INSERTION FOR DEADLOCK-FREE ROUTING
ON ARBITRARY
Attorney's Docket : SUNM-058XX

Group Art Unit: 2181

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D.C. 20231 on 3/15/03.

By: 

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L E T T E R

Commissioner for Patents
Office of Initial Patent Examination
Customer Service Center
Washington, D.C. 20231

Sir:

In reviewing the Filing Receipt (copy enclosed) for the
above-identified patent application, the following error was
noted:

Application No.: 10/027,886
Filed: October 19, 2001
Group Art Unit: 2181

Title

Efficient system and method of node and link insertion for
deadlock-free routing on **arbitarary** topologies

should read:

Title

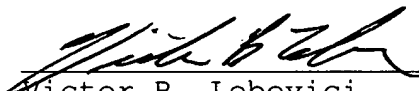
Efficient system and method of node and link insertion for
deadlock-free routing on **arbitrary** topologies

Kindly correct the error of the Patent and Trademark Office
and issue a corrected Filing Receipt.

Respectfully submitted,

JOHN V. REYNDERS, ET AL.

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Enclosure
287254



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APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLAIMS	IND CLAIMS
10/027,886	10/19/2001	2181	1196	P6270	10	22	8

207

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Assignment For Published Patent Application

Sun Microsystems, Inc.;

Domestic Priority data as claimed by applicant

Foreign Applications

If Required, Foreign Filing License Granted 02/05/2002

Projected Publication Date: 04/24/2003

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Early Publication Request: No

Title

Efficient system and method of node and link insertion for deadlock-free routing on arbitrary

arbitrary!

S.
4/17/03
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topologies

Preliminary Class

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